# DIM800DCS12-A000

## IGBT Chopper Module

DS5839- 1.1 June 2005 (LN24042)

### FEATURES

- 10µs Short Circuit Withstand
- Non Punch Through Silicon
- Isolated Copper Baseplate
- Lead Free construction

### APPLICATIONS

- Chopper
- DC Motor Drives
- Power Supplies

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 600V to 3300V and currents up to 2400A.

The DIM800DCS12-A000 is a 1200V, n channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus full 10µs short circuit withstand.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

### ORDERING INFORMATION

Order As:

**DIM800DCS12-A000**

Note: When ordering, please use the whole part number.

### KEY PARAMETERS

| | | |
|---|---|---|
| $V_{CES}$ | | 1200V |
| $V_{CE\ (sat)}$* | (typ) | 2.2V |
| $I_C$ | (max) | 800A |
| $I_{C(PK)}$ | (max) | 1600A |

*(measured at the power busbars and not the auxiliary terminals)

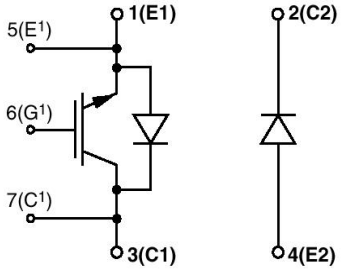
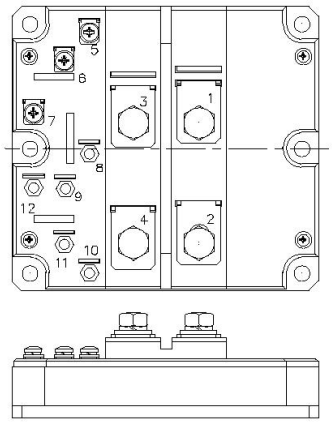



**Fig. 1 Chopper circuit diagram**



**Outline type code: D**

**(See package details for further information)**

**Fig. 2 Electrical connections (not to scale)**

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## ABSOLUTE MAXIMUM RATINGS – PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

**Tcase = 25° C unless stated otherwise**

| Symbol | Parameter | Test Conditions | Max. | Units |
|---|---|---|---|---|
| $V_{CES}$ | Collector-emitter voltage | $V_{GE}$ = 0V | 1200 | V |
| $V_{GES}$ | Gate-emitter voltage | | ±20 | V |
| $I_C$ | Continuous collector current | $T_{case}$ = 85° C | 800 | A |
| $I_{C(PK)}$ | Peak collector current | 1ms, $T_{case}$ =115° C | 1600 | A |
| $P_{max}$ | Max. transistor power dissipation | $T_{case}$ = 25° C, $T_j$ = 150° C | 6940 | W |
| $I^2t$ | Diode $I^2t$ value (IGBT arm) | $V_R$ = 0, $t_P$ = 10ms, $T_{vj}$ = 125° C | 100 | $kA^2S$ |
| | Diode $I^2t$ value (Diode arm) | | 225 | |
| $V_{isol}$ | Isolation voltage – per module | Commoned terminals to base plate. AC RMS, 1 min, 50Hz | 2500 | V |

## THERMAL AND MECHANICAL RATINGS

Internal insulation material: $Al_2O_3$
Baseplate material: Copper
Creepage distance: 20mm
Clearance: 10mm
CTI (Critical Tracking Index): 175

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|---|---|---|---|---|---|---|
| $R_{th(j-c)}$ | Thermal resistance – transistor | Continuous dissipation – junction to case | - | - | 18 | ° C/kW |
| $R_{th(j-c)}$ | Thermal resistance – diode (IGBT arm)<br>Thermal resistance- diode ( Diode arm) | Continuous dissipation – junction to case | - | - | 40<br>27 | ° C/kW<br>° C/kW |
| $R_{th(c-h)}$ | Thermal resistance – case to heatsink (per module) | Mounting torque 5Nm (with mounting grease) | - | - | 8 | ° C/kW |
| $T_j$ | Junction temperature | Transistor | - | - | 150 | ° C |
| | | Diode | - | - | 125 | ° C |
| $T_{stg}$ | Storage temperature range | - | -40 | - | 125 | ° C |
| - | Screw torque | Mounting – M6 | - | - | 5 | Nm |
| | | Electrical connections – M4 | - | - | 2 | Nm |
| | | Electrical connections – M8 | - | - | 10 | Nm |

## ELECTRICAL CHARACTERISTICS

**$T_{case}$ = 25° C unless stated otherwise.**

| Symbol | Parameter | Test Conditions | | Min. | Typ. | Max. | Units |
|---|---|---|---|---|---|---|---|
| $I_{ces}$ | Collector cut-off current | $V_{GE}$ = OV, $V_{CE}$ = $V_{CES}$ | | - | - | 1 | mA |
| | | $V_{GE}$ = OV, $V_{CE}$ = $V_{CES}$, $T_{case}$ = 125° C | | - | - | 25 | mA |
| $I_{ces}$ | Gate leakage current | $V_{GE}$ = ±20V, $V_{CE}$ = 0V | | - | - | 4 | µA |
| $V_{GE(TH)}$ | Gate threshold voltage | $I_C$ = 40mA, $V_{GE}$ = $V_{CE}$ | | 4.5 | 5.5 | 6.5 | V |
| $V_{CE(sat)}$† | Collector-emitter saturation voltage | $V_{GE}$ = 15V, $I_C$ = 800A | | - | 2.2 | 2.8 | V |
| | | $V_{GE}$ = 15V, $I_C$ = 800A, $T_{case}$ = 125° C | | - | 2.6 | 3.2 | V |
| $I_F$ | Diode forward current | DC | | - | - | 800 | A |
| $I_{FM}$ | Diode maximum forward current | $t_p$ = 1ms | | - | - | 1600 | A |
| $V_F$† | Diode forward voltage (IGBT arm)<br>Diode forward voltage (Diode arm) | $I_F$ = 800A | | - | 2.1<br>1.8 | 2.4<br>2.1 | V |
| | Diode forward voltage (IGBT arm)<br>Diode forward voltage (Diode arm) | $I_F$ = 800A, $T_{case}$ = 125° C | | - | 2.1<br>1.7 | 2.4<br>2.0 | V |
| $C_{ies}$ | Input capacitance | $V_{CE}$ = 25V, $V_{GE}$ = 0V, f = 1MHz | | - | 90 | - | nF |
| $L_M$ | Module inductance – per arm | - | | - | 20 | - | nH |
| $R_{INT}$ | Internal resistance – per arm | - | | - | 0.27 | - | mΩ |
| $SC_{Data}$ | Short circuit. $I_{sc}$ | $T_j$ = 125° C, $V_{cc}$ = 900V, | $I_1$ | - | 5500 | - | A |
| | | $t_p \leq 10\mu s$,<br>$V_{CE(max)} = V_{CES} - L^* \times di/dt$<br>IEC 60747-9 | $I_2$ | - | 4500 | - | A |

**Note:**
† Measured at the power busbars and not the auxiliary terminals
* L is the circuit inductance + $L_M$

## ELECTRICAL CHARACTERISTICS

**$T_{case}$ = 25° C unless stated otherwise.**

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|---|---|---|---|---|---|---|
| $t_{d(off)}$ | Turn-off delay time | $I_C$ = 800A | - | 1250 | - | ns |
| $t_f$ | Fall time | $V_{GE}$ = ±15V | - | 170 | - | ns |
| $E_{OFF}$ | Turn-off energy loss | $V_{CE}$ = 600V | - | 130 | - | mJ |
| $t_{d(on)}$ | Turn-on delay time | $R_{G(ON)}$ = $R_{G(OFF)}$ = 2.7Ω | - | 250 | - | ns |
| $t_r$ | Rise time | L ~ 100nH | - | 250 | - | ns |
| $E_O$ | Turn-on energy loss | | - | 80 | - | mJ |
| $Q_g$ | Gate charge | | - | 9 | - | µC |
| $Q_{rr}$ | Diode reverse recovery charge | $I_F$ = 800A, $V_R$ = 600V, | - | 12 | - | µC |
| $I_{rr}$ | Diode reverse current | $dI_F/dt$ = 4200A/µs | - | 570 | - | A |
| $E_{REC}$ | Diode reverse recovery energy | Diode arm | - | 60 | - | mJ |

**$T_{case}$ = 125° C unless stated otherwise.**

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|---|---|---|---|---|---|---|
| $t_{d(off)}$ | Turn-off delay time | $I_C$ = 800A | - | 1500 | - | ns |
| $t_f$ | Fall time | $V_{GE}$ = ±15V | - | 200 | - | ns |
| $E_{OFF}$ | Turn-off energy loss | $V_{CE}$ = 600V | - | 160 | - | mJ |
| $t_{d(on)}$ | Turn-on delay time | $R_{G(ON)}$ = $R_{G(OFF)}$ = 2.7Ω | - | 400 | - | ns |
| $t_r$ | Rise time | L ~ 100nH | - | 220 | - | ns |
| $E_{ON}$ | Turn-on energy loss | | - | 120 | - | mJ |
| $Q_{rr}$ | Diode reverse recovery charge | $I_F$ = 800A, $V_R$ = 600V, | - | 240 | - | µC |
| $I_{rr}$ | Diode reverse current | $dI_F/dt$ = 4000A/µs | - | 680 | - | A |
| $E_{REC}$ | Diode reverse recovery energy | Diode arm | - | 110 | - | mJ |

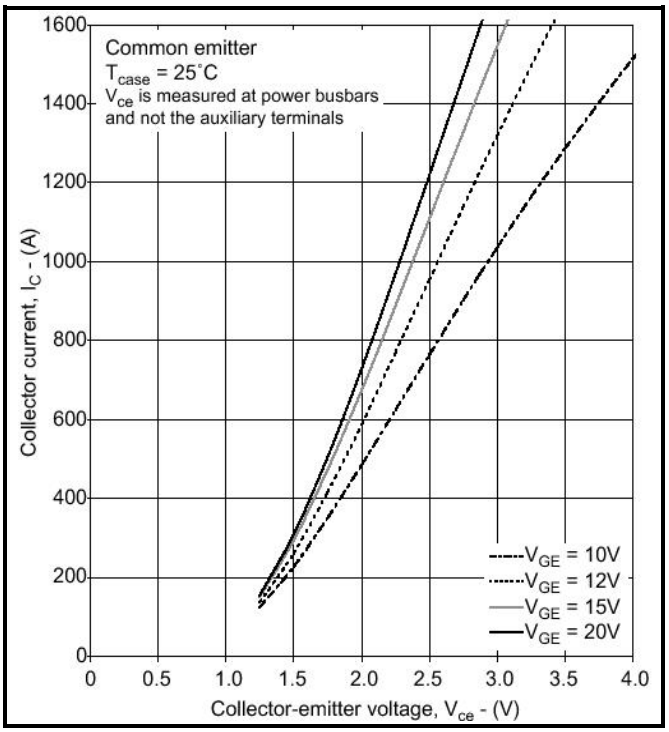



**Fig.3 Typical output characteristics**

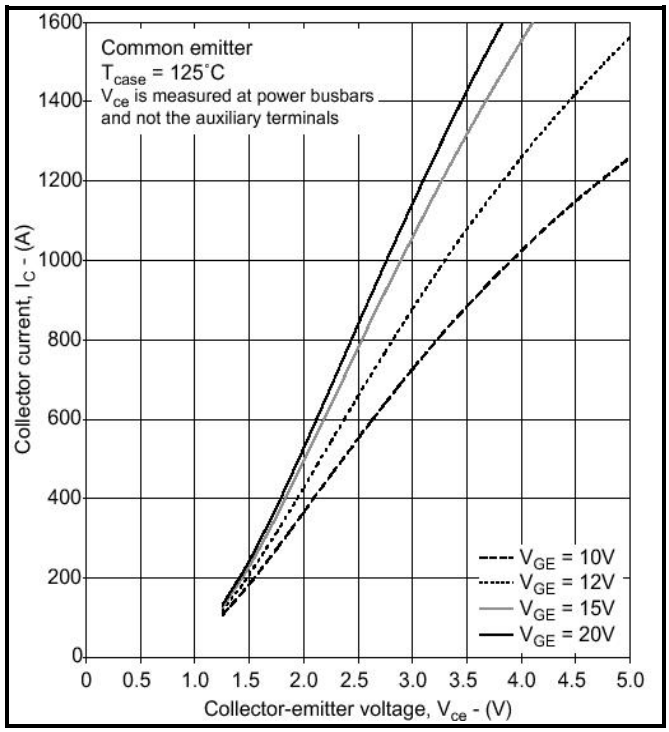



**Fig.4 Typical output characteristics**

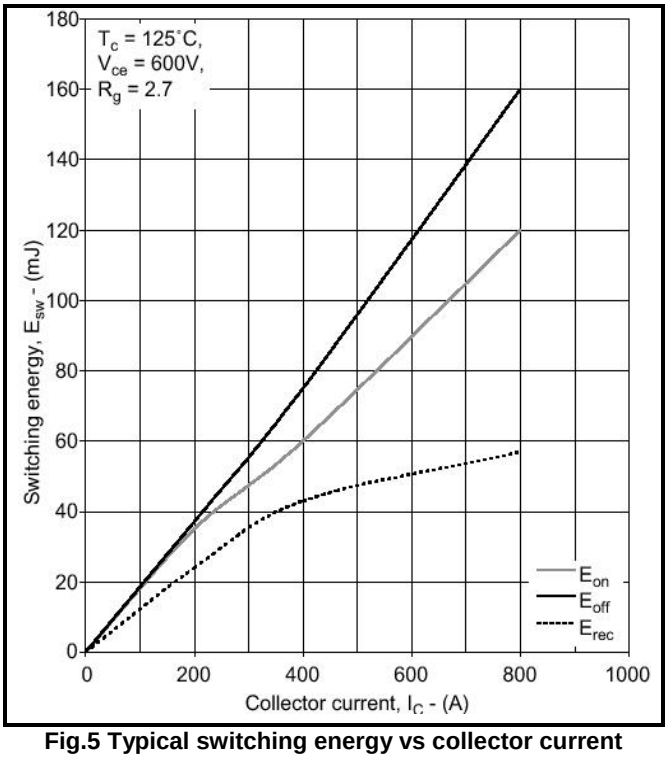



**Fig.5 Typical switching energy vs collector current**

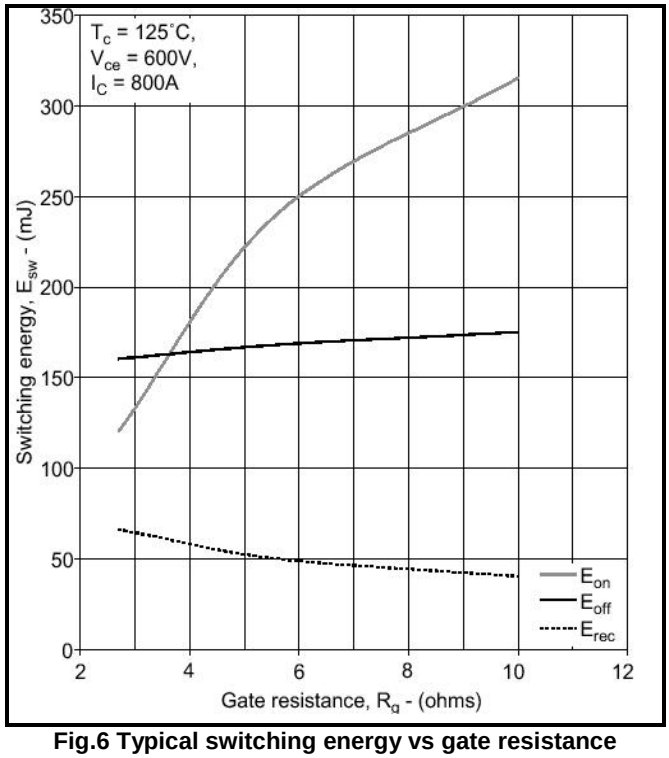



**Fig.6 Typical switching energy vs gate resistance**

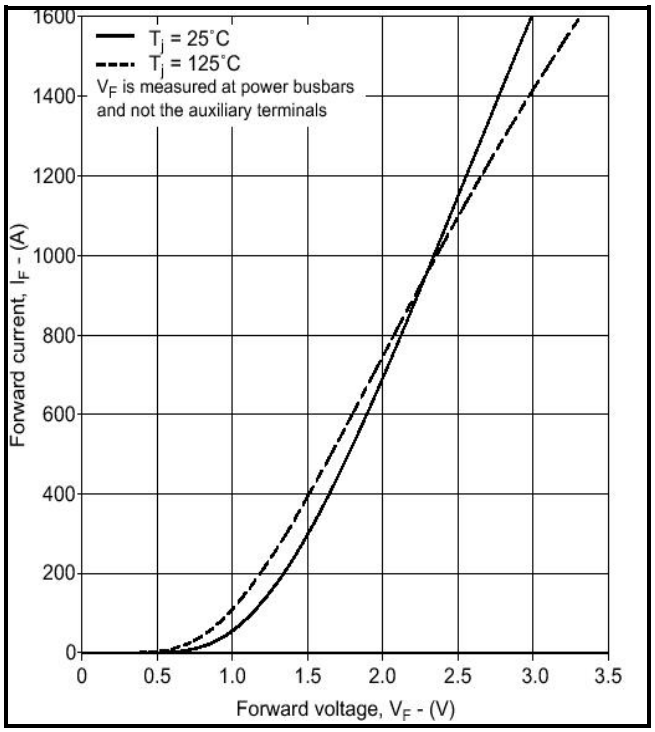



**Fig.7 Diode typical forward characteristics**

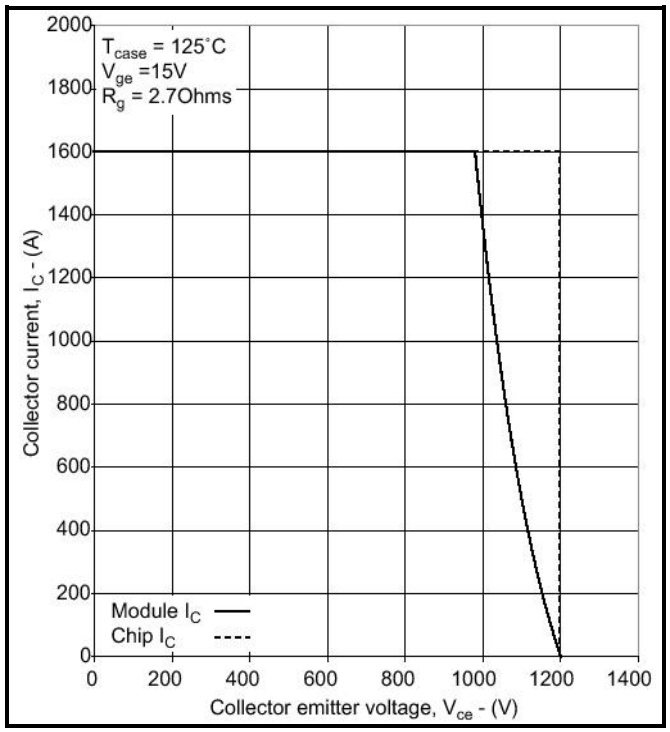



**Fig.8 Reverse bias safe operating area**

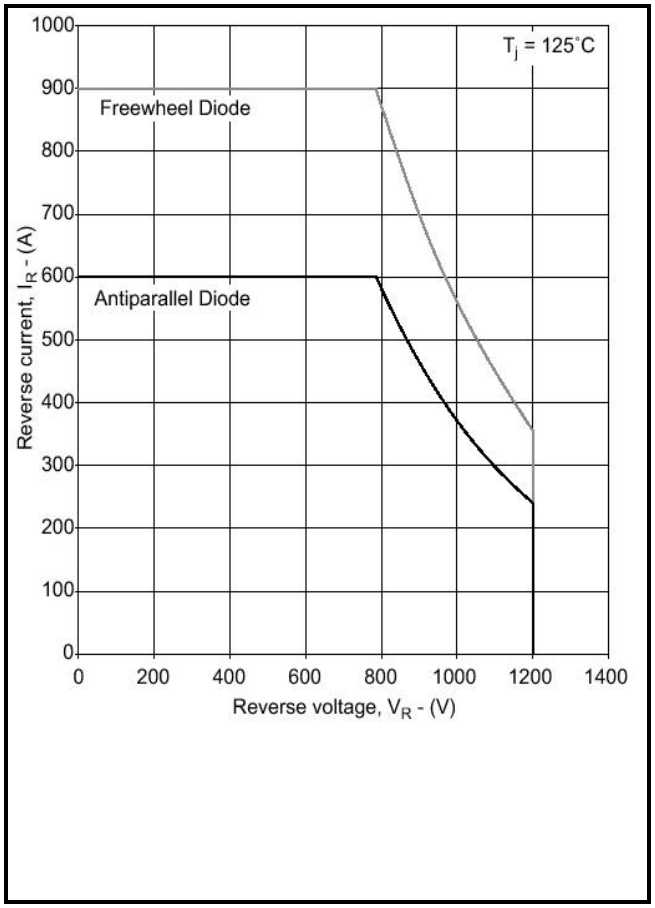
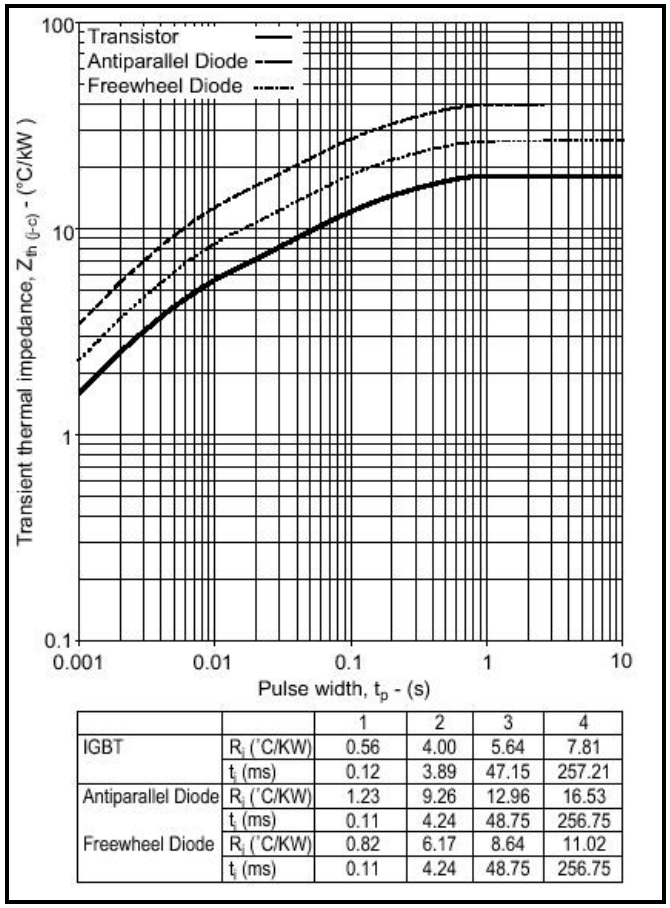



**Fig.9 Diode reverse bias safe operating area**

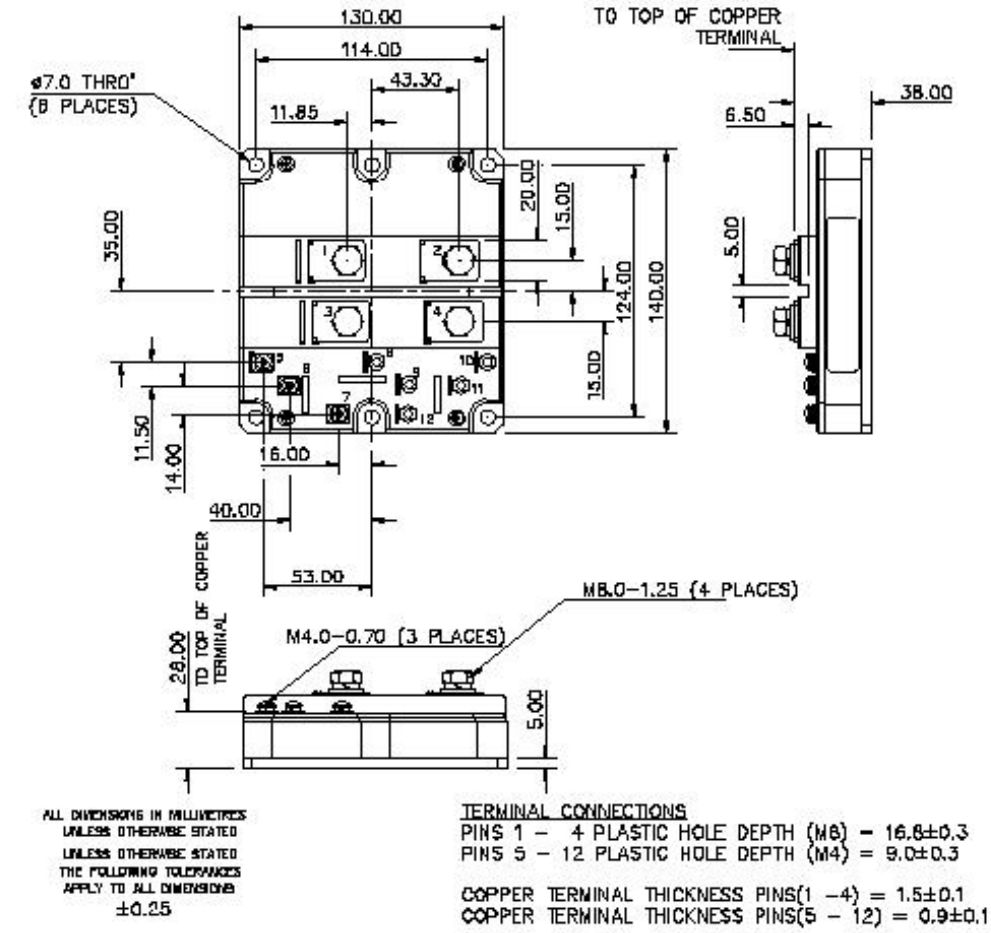



| | | 1 | 2 | 3 | 4 |
|---|---|---|---|---|---|
| IGBT | $R_i$ (˚C/KW) | 0.56 | 4.00 | 5.64 | 7.81 |
| | $t_i$ (ms) | 0.12 | 3.89 | 47.15 | 257.21 |
| Antiparallel Diode | $R_i$ (˚C/KW) | 1.23 | 9.26 | 12.96 | 16.53 |
| | $t_i$ (ms) | 0.11 | 4.24 | 48.75 | 256.75 |
| Freewheel Diode | $R_i$ (˚C/KW) | 0.82 | 6.17 | 8.64 | 11.02 |
| | $t_i$ (ms) | 0.11 | 4.24 | 48.75 | 256.75 |

**Fig.10 Transient thermal impedance**

## PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.
DO NOT SCALE.




Nominal weight: 1600 gms

**Module outline type code: D**

## POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

## HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.

http://www.dynexsemi.com

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Datasheet Annotations:

Dynex Semiconductor annotate datasheets in the top right hand corner of the front page, to indicate product status. The annotations are as follows:-

**Target Information:** This is the most tentative form of information and represents a very preliminary specification. No actual design work on the product has been started.

**Preliminary Information:** The product is in design and development. The datasheet represents the product as it is understood but details may change.

**Advance Information:** The product design is complete and final characterisation for volume production is well in hand.

**No Annotation:** The product parameters are fixed and the product is available to datasheet specification.