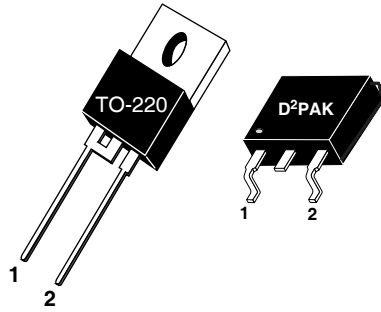


1 - Cathode
2 - Anode
Back of Case - Cathode



**ADVANCED
POWER
TECHNOLOGY®**

APT15D60K 600V 15A
APT15D60SA 600V 15A

ULTRAFAST SOFT RECOVERY RECTIFIER DIODE

PRODUCT APPLICATIONS

- Anti-Parallel Diode
 - Switchmode Power Supply
 - Inverters
- Free Wheeling Diode
 - Motor Controllers
 - Converters
- Snubber Diode
- Uninterruptible Power Supply (UPS)
- Induction Heating
- High Speed Rectifiers

PRODUCT FEATURES

- Ultrafast Recovery Times
- Soft Recovery Characteristics
- Popular TO-220 or TO-263 (D2) Surface Mount Package
- Low Forward Voltage
- High Blocking Voltage
- Low Leakage Current

PRODUCT BENEFITS

- Low Losses
- Low Noise Switching
- Cooler Operation
- Higher Reliability Systems
- Increased System Power Density

MAXIMUM RATINGS

All Ratings: $T_C = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Characteristic / Test Conditions	APT15D60K_SA	UNIT
V_R	Maximum D.C. Reverse Voltage	600	Volts
V_{RRM}	Maximum Peak Repetitive Reverse Voltage		
V_{RWM}	Maximum Working Peak Reverse Voltage		
$I_F(AV)$	Maximum Average Forward Current ($T_C = 108^\circ\text{C}$, Duty Cycle = 0.5)	15	Amps
$I_F(RMS)$	RMS Forward Current (Square wave, 50% duty)	28	
I_{FSM}	Non-Repetitive Forward Surge Current ($T_J = 45^\circ\text{C}$, 8.3ms)	110	
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_L	Lead Temperature for 10 Sec.	300	

STATIC ELECTRICAL CHARACTERISTICS

Symbol		MIN	TYP	MAX	UNIT
V_F	Forward Voltage	$I_F = 15\text{A}$	1.6	1.8	Volts
		$I_F = 30\text{A}$	1.9		
		$I_F = 15\text{A}, T_J = 125^\circ\text{C}$	1.4		
I_{RM}	Maximum Reverse Leakage Current	$V_R = V_R$ Rated		150	μA
		$V_R = V_R$ Rated, $T_J = 125^\circ\text{C}$		500	
C_T	Junction Capacitance, $V_R = 200\text{V}$		23		pF

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
t_{rr}	Reverse Recovery Time	$I_F = 1A, di_F/dt = -100A/\mu s, V_R = 30V, T_J = 25^\circ C$	-	21		ns
t_{rr}	Reverse Recovery Time		-	80		
Q_{rr}	Reverse Recovery Charge	$I_F = 15A, di_F/dt = -200A/\mu s, V_R = 400V, T_C = 25^\circ C$	-	95		nC
I_{RRM}	Maximum Reverse Recovery Current		-	3	-	Amps
t_{rr}	Reverse Recovery Time		-	150		ns
Q_{rr}	Reverse Recovery Charge	$I_F = 15A, di_F/dt = -200A/\mu s, V_R = 400V, T_C = 125^\circ C$	-	520		nC
I_{RRM}	Maximum Reverse Recovery Current		-	7	-	Amps
t_{rr}	Reverse Recovery Time		-	60		ns
Q_{rr}	Reverse Recovery Charge	$I_F = 15A, di_F/dt = -1000A/\mu s, V_R = 400V, T_C = 125^\circ C$	-	810		nC
I_{RRM}	Maximum Reverse Recovery Current		-	22		Amps

THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction-to-Case Thermal Resistance			1.35	$^\circ C/W$
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance			80	
W_T	Package Weight		0.07		oz
			1.9		g
Torque	Maximum Mounting Torque			10	lb•in
				1.1	N•m

APT Reserves the right to change, without notice, the specifications and information contained herein.

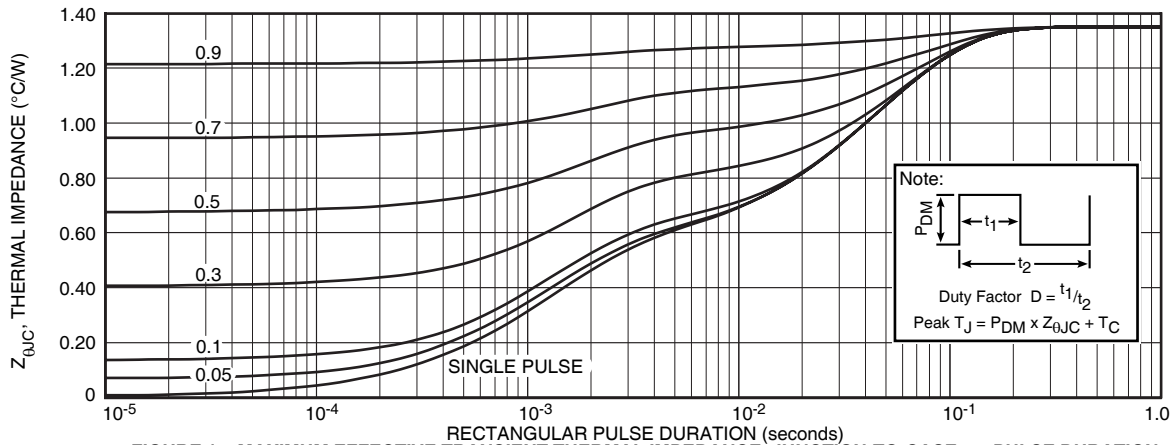


FIGURE 1a. MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs. PULSE DURATION

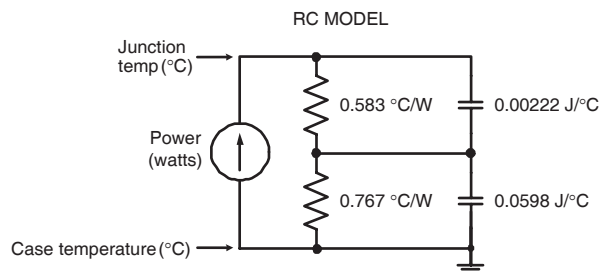


FIGURE 1b, TRANSIENT THERMAL IMPEDANCE MODEL

TYPICAL PERFORMANCE CURVES

APT15D60K_SA

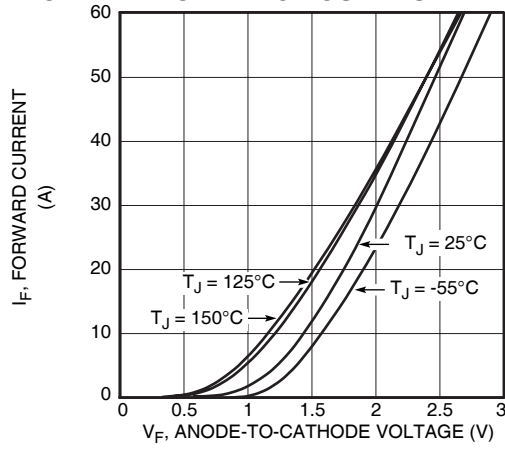


Figure 2. Forward Current vs. Forward Voltage

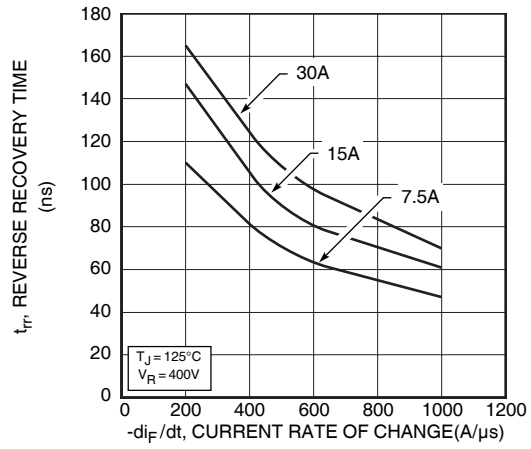


Figure 3. Reverse Recovery Time vs. Current Rate of Change

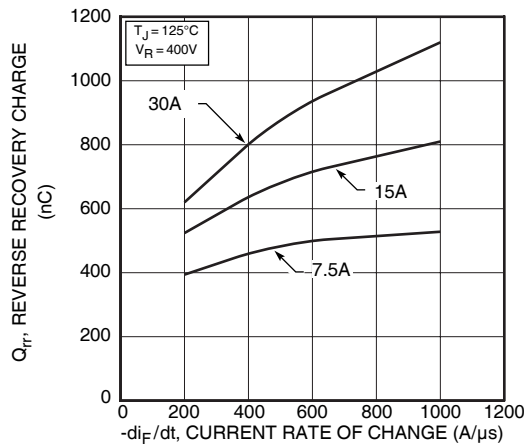


Figure 4. Reverse Recovery Charge vs. Current Rate of Change

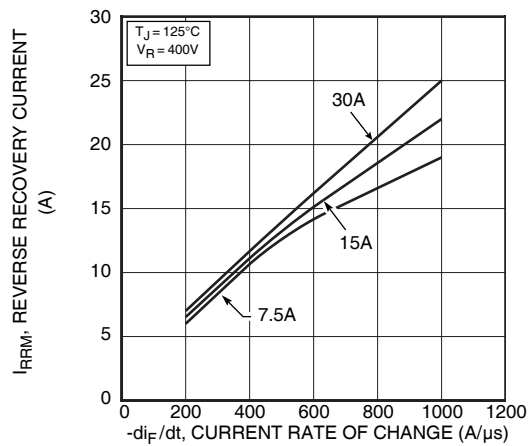


Figure 5. Reverse Recovery Current vs. Current Rate of Change

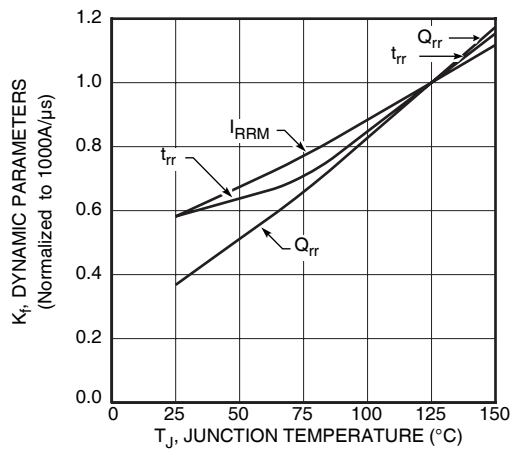


Figure 6. Dynamic Parameters vs. Junction Temperature

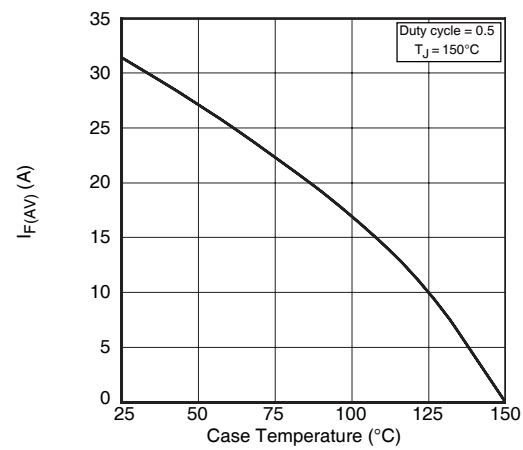


Figure 7. Maximum Average Forward Current vs. Case Temperature

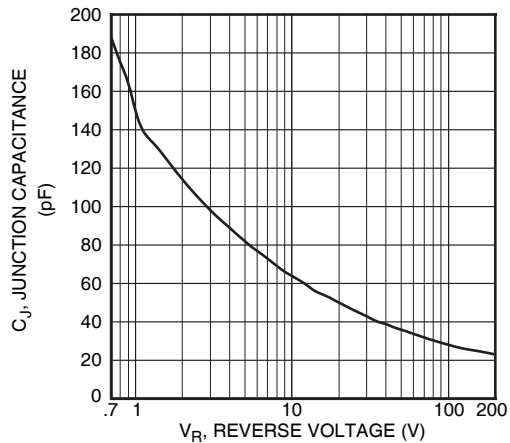


Figure 8. Junction Capacitance vs. Reverse Voltage

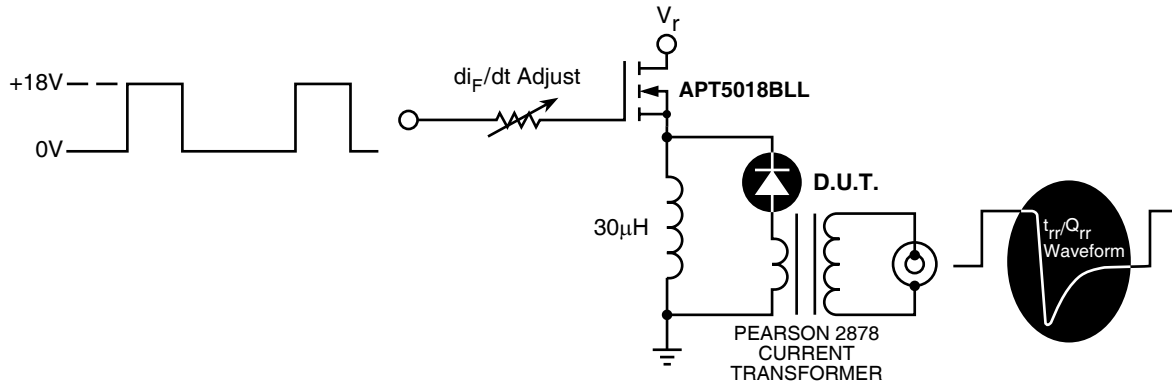


Figure 9. Diode Test Circuit

- 1 I_F - Forward Conduction Current
- 2 di_F/dt - Rate of Diode Current Change Through Zero Crossing.
- 3 I_{RRM} - Maximum Reverse Recovery Current.
- 4 t_{rr} - Reverse Recovery Time, measured from zero crossing where diode current goes from positive to negative, to the point at which the straight line through I_{RRM} and $0.25 \cdot I_{RRM}$ passes through zero.
- 5 Q_{rr} - Area Under the Curve Defined by I_{RRM} and t_{rr} .

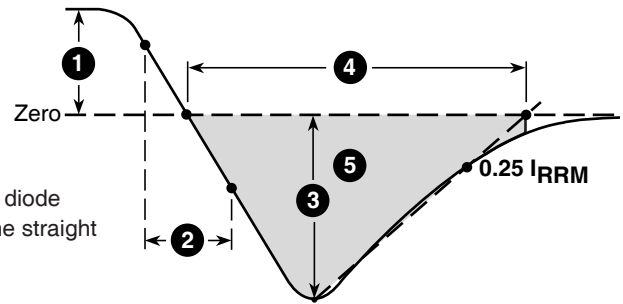
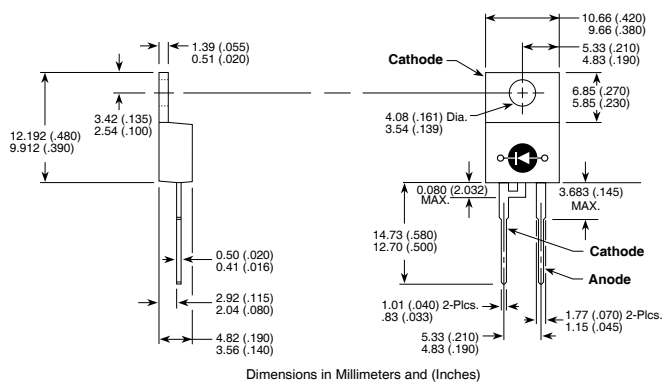
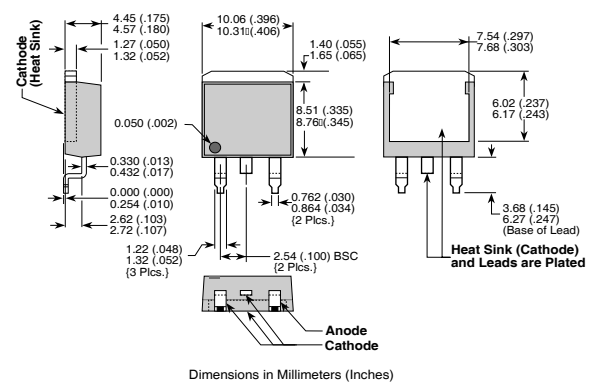


Figure 10, Diode Reverse Recovery Waveform and Definitions

TO-220AB Package Outline



TO-263 (D²) Surface mount Package outline



053-6010 Rev I 6-2004

APT's products are covered by one or more of U.S. patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. US and Foreign patents pending. All Rights Reserved.